

P2010-B01

75W/80W/100W GK107/GK106/GM107 128-BIT GDDR5 in x32/x16 MODE
DVI-I-DL + DVI-D-DL/HDMI + mHDMI/DP


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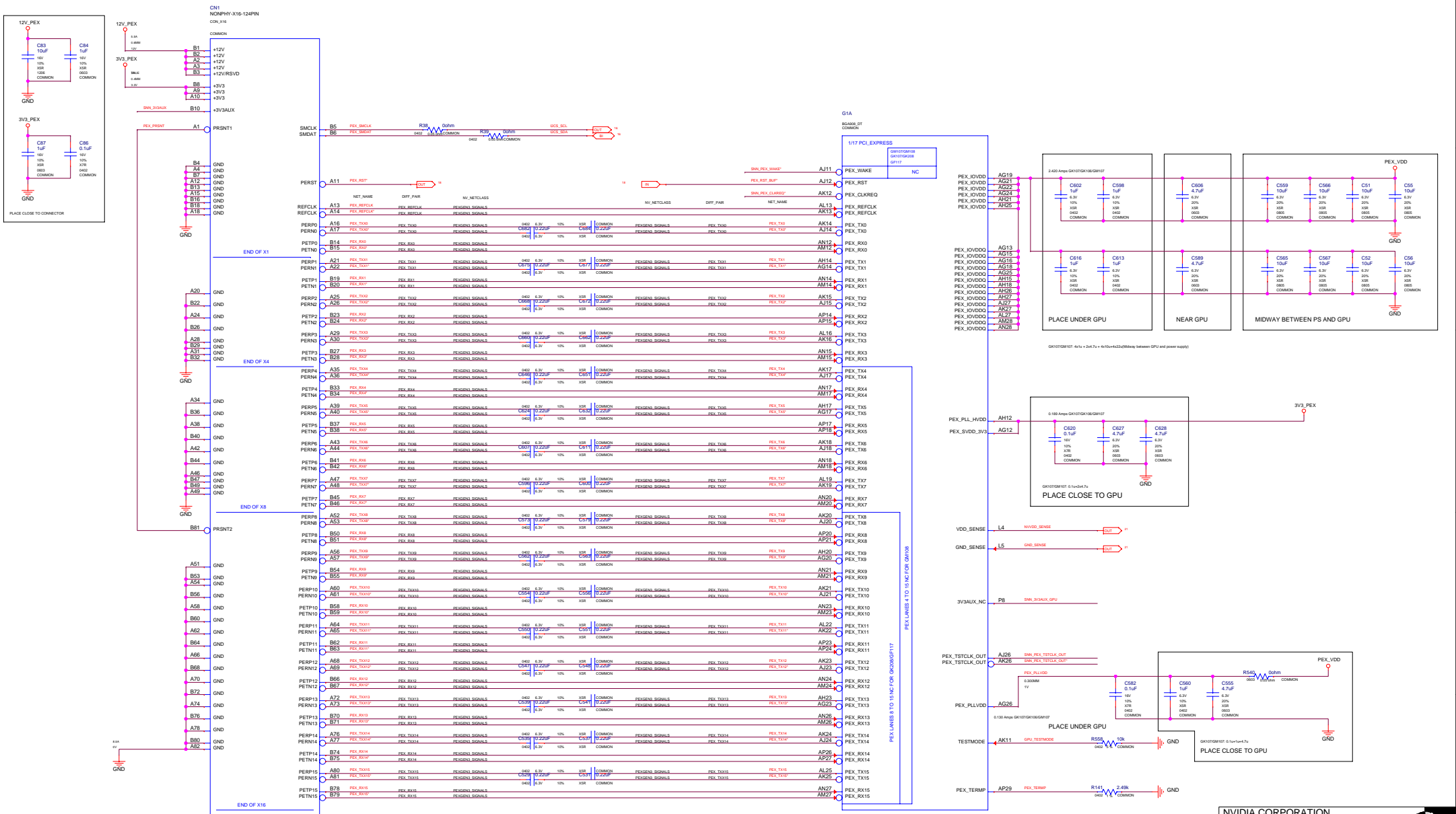
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PCI Express



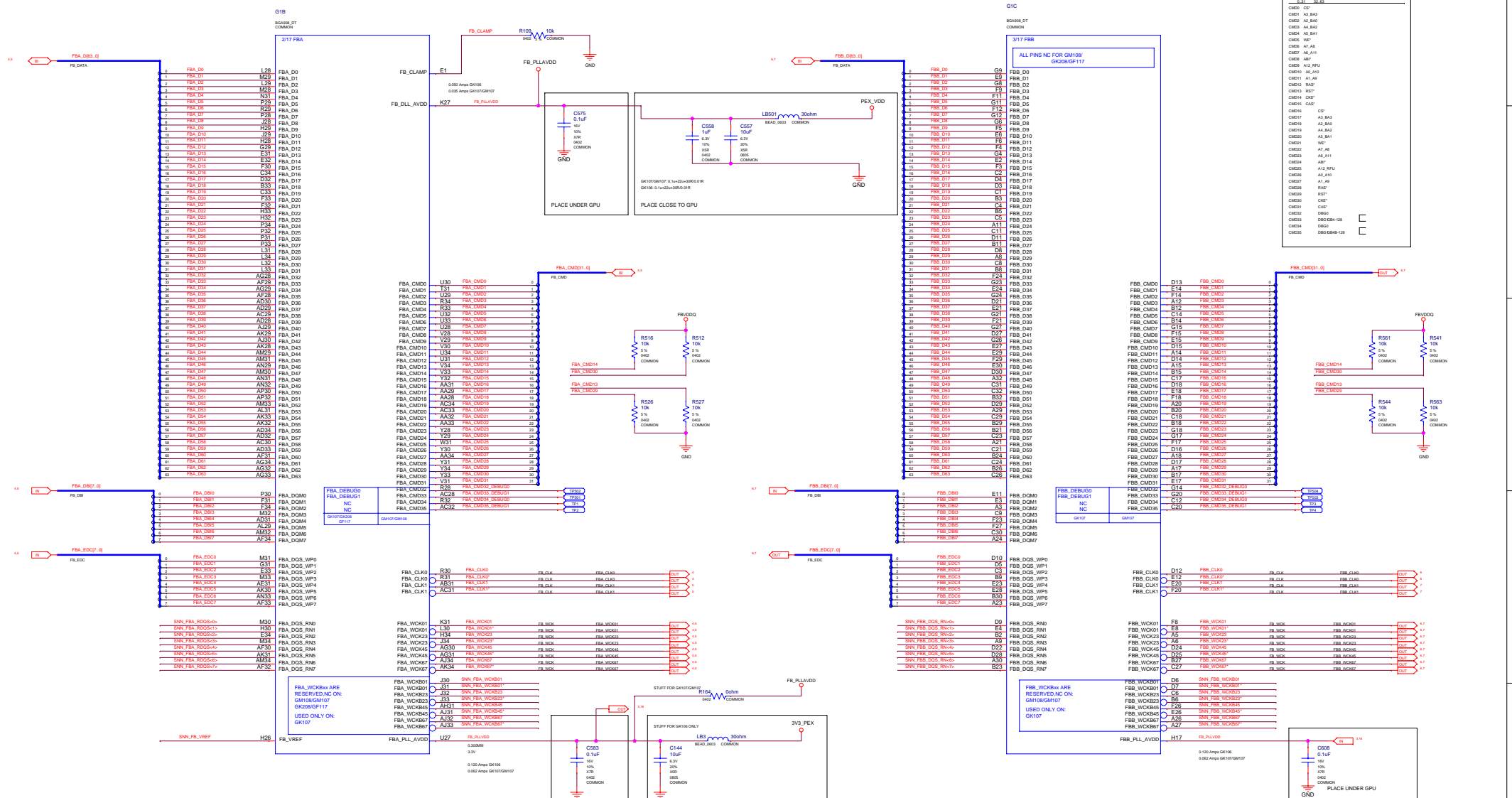
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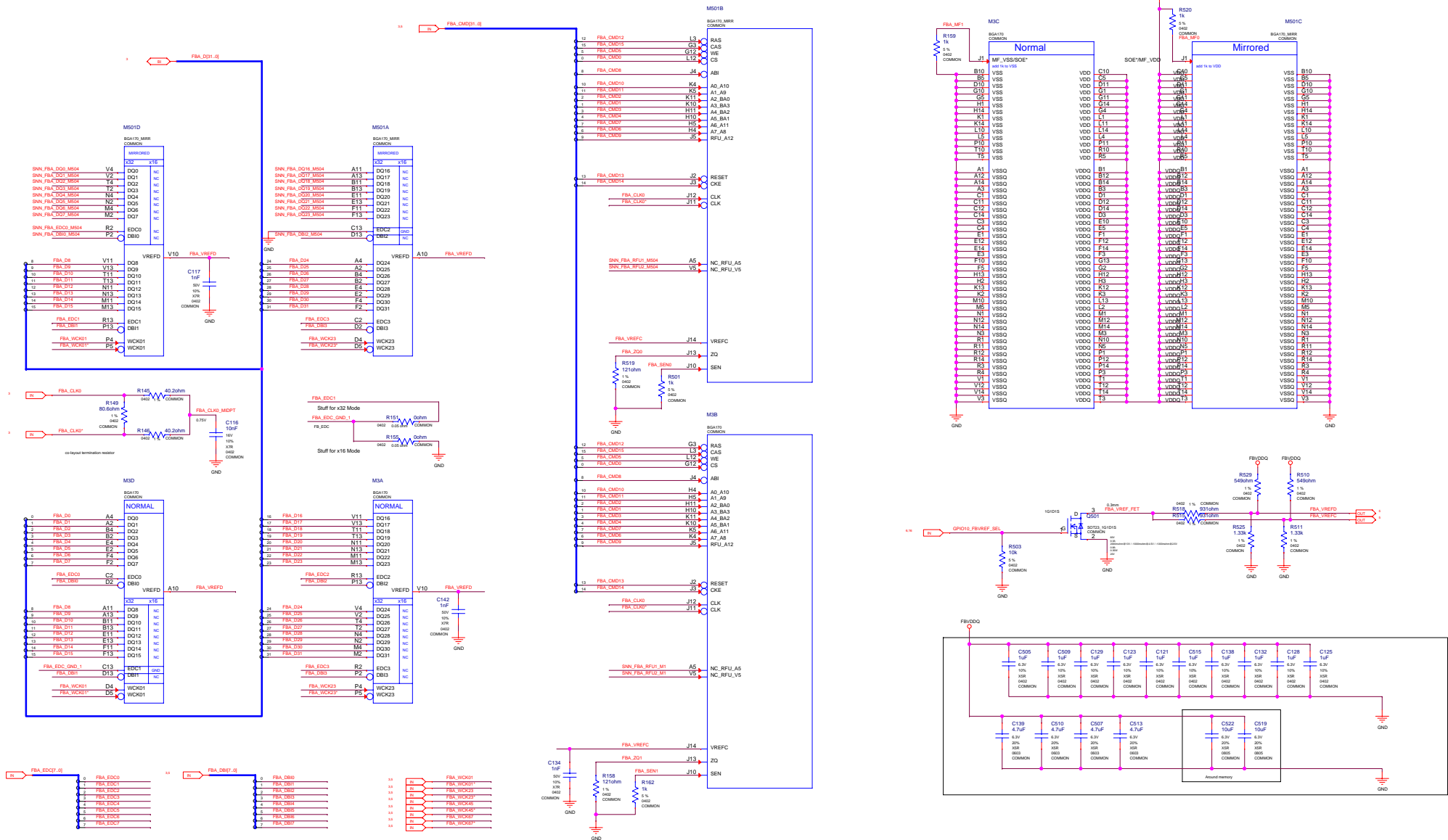
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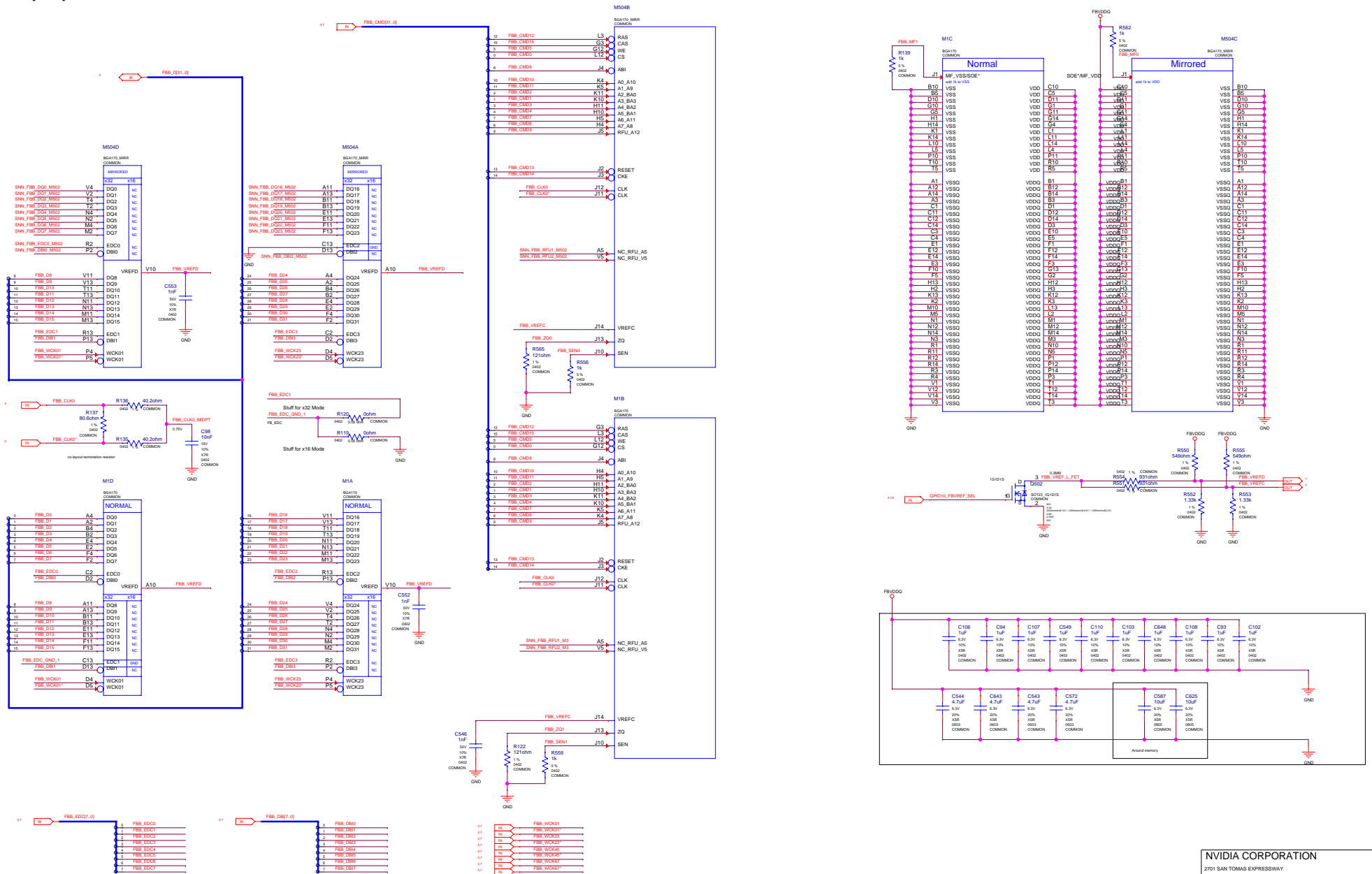
GPU Frame Buffer Partition A/B



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FBB Partition [31..0]



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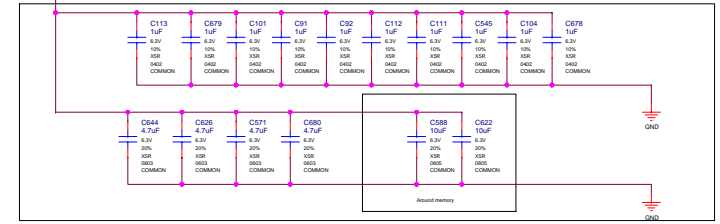
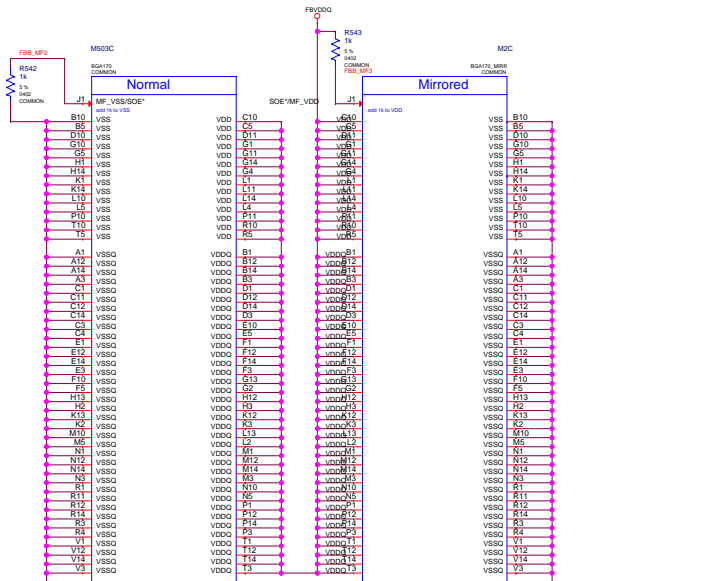
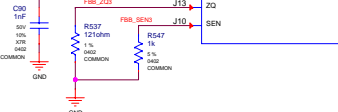
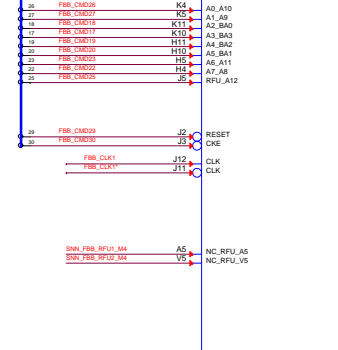
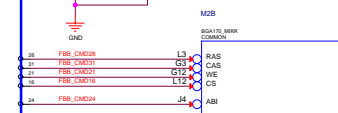
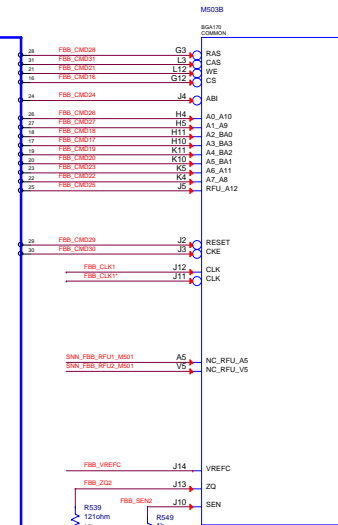
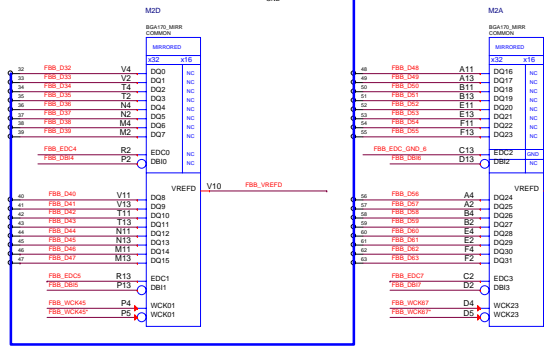
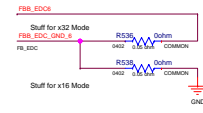
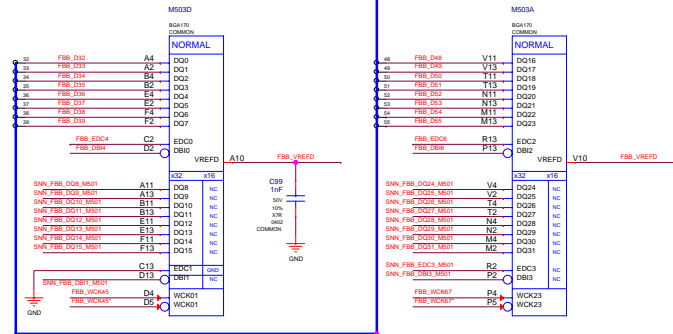
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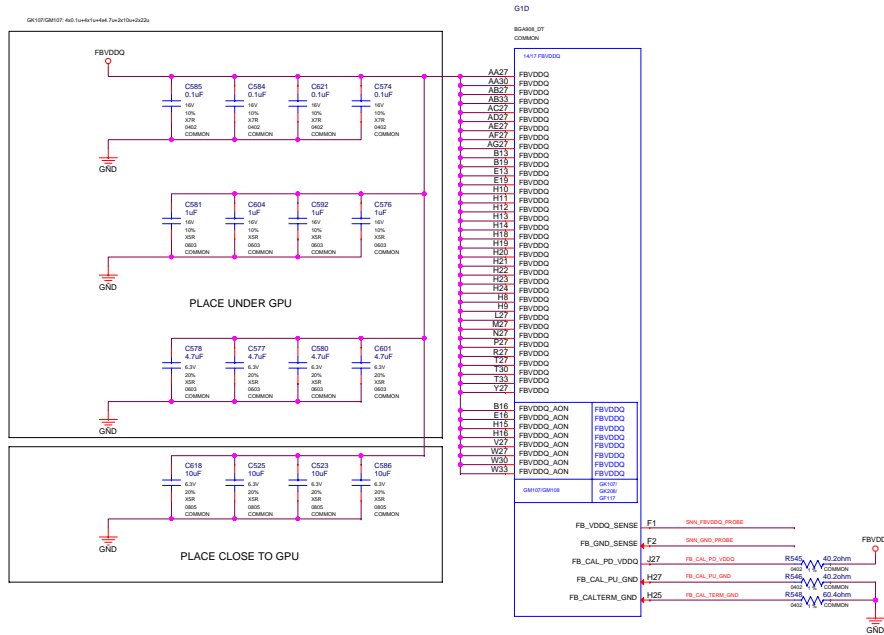
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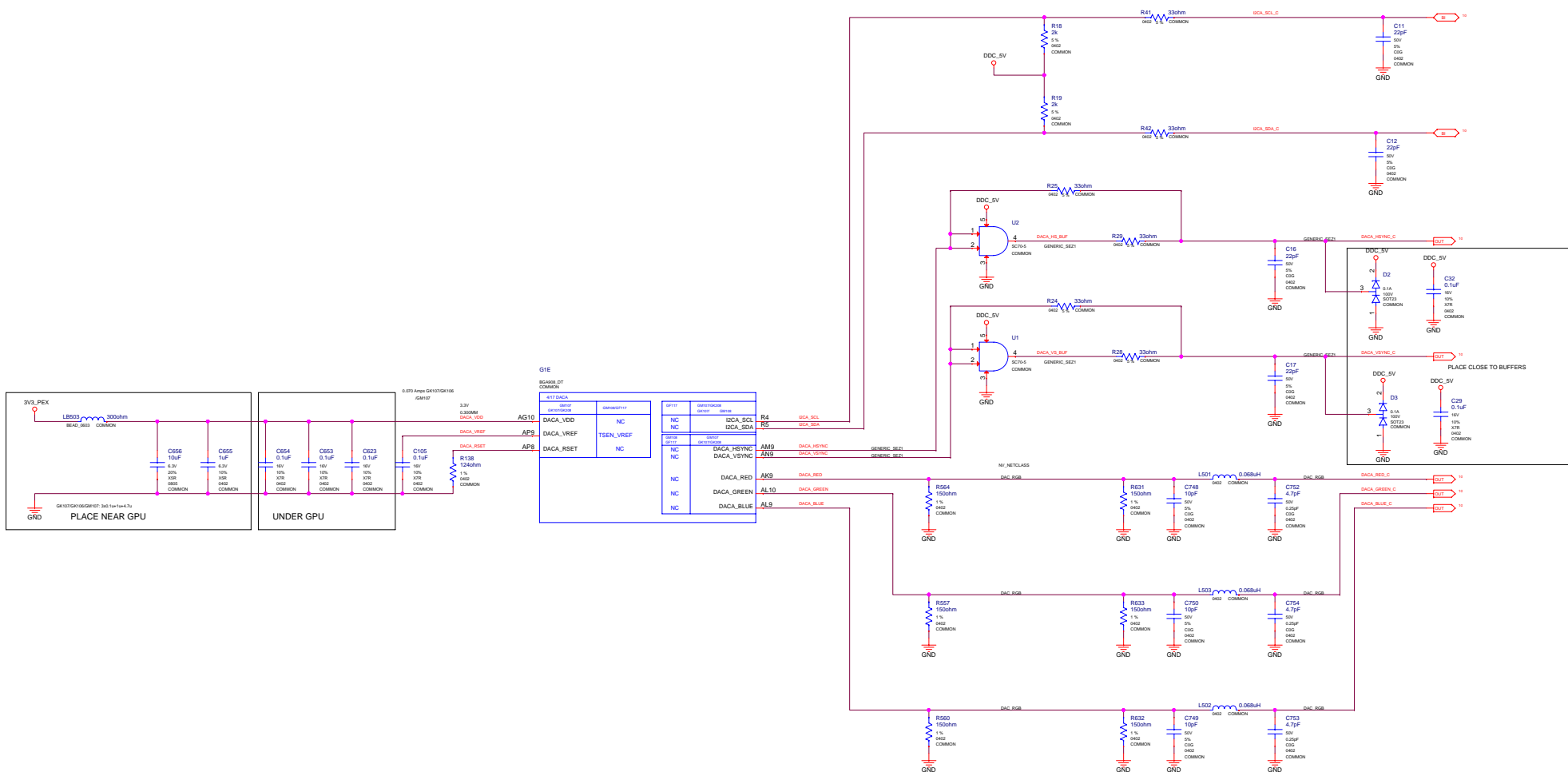
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GPU FB Decoupling



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DACA VGA



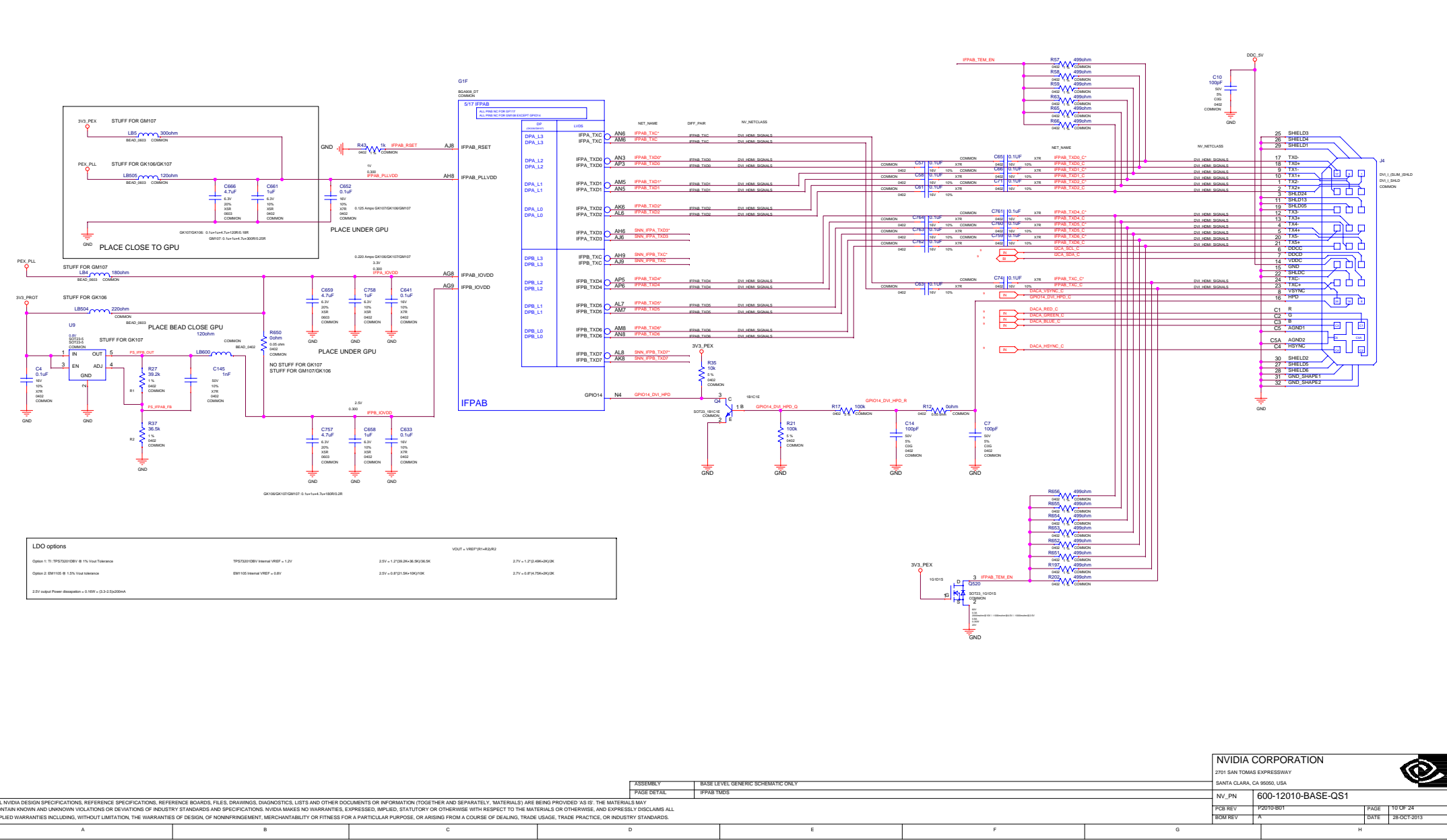
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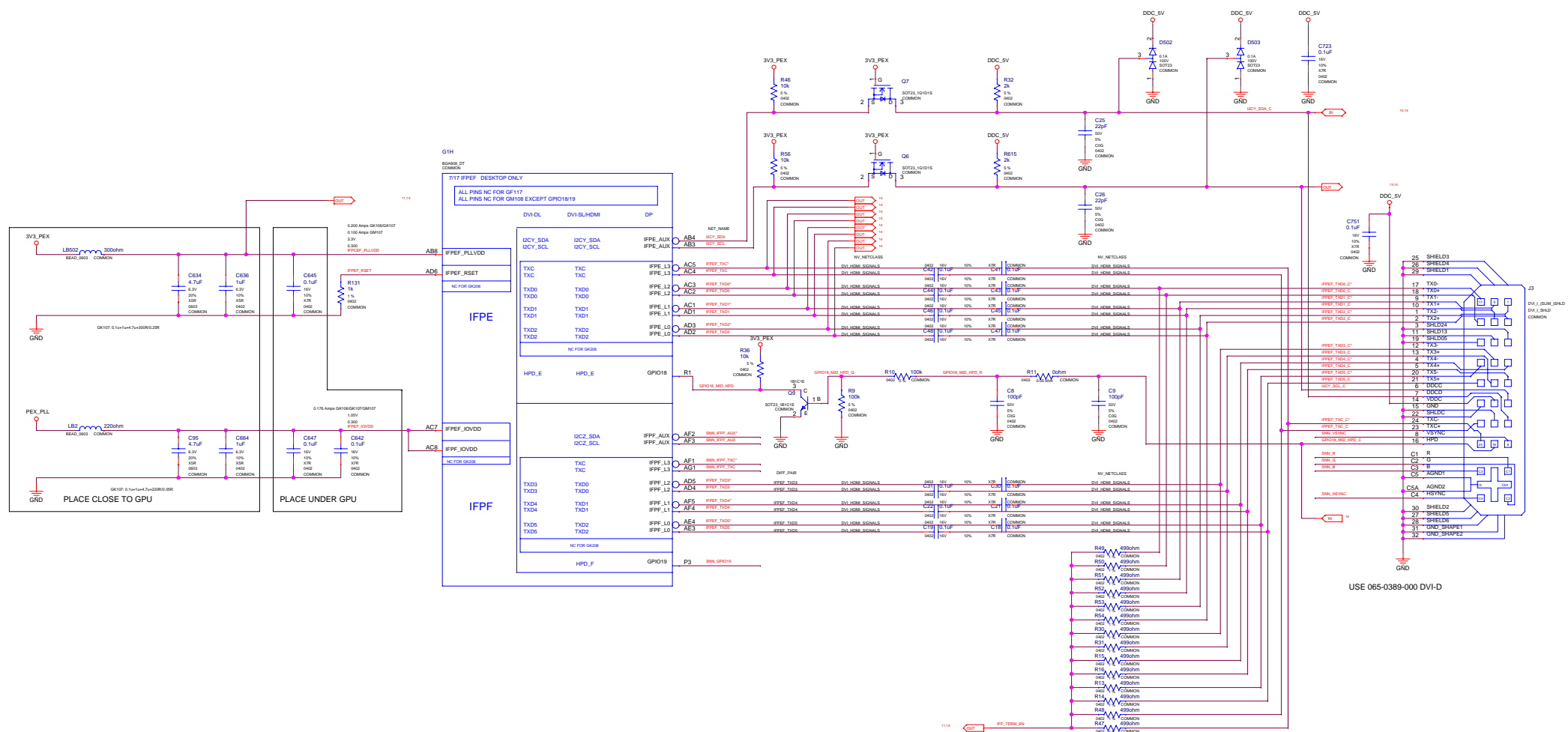
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IFPEF TMDS

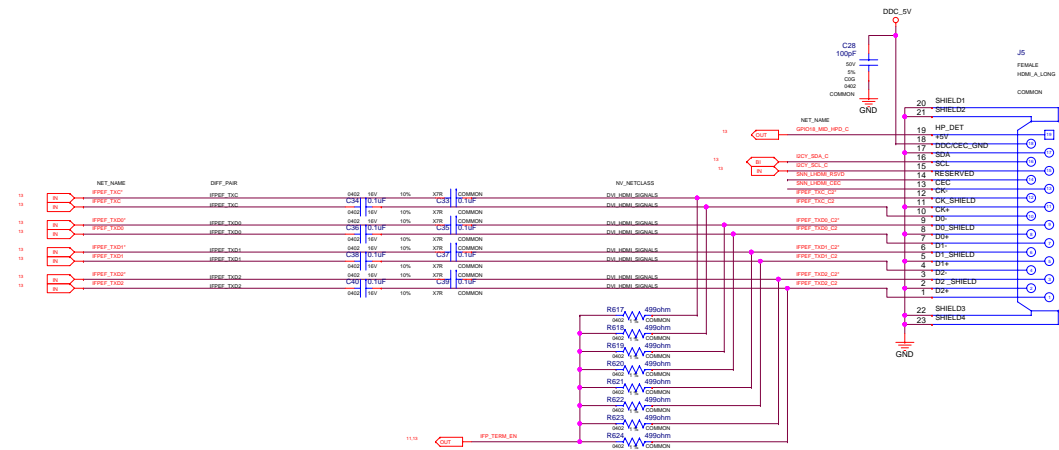



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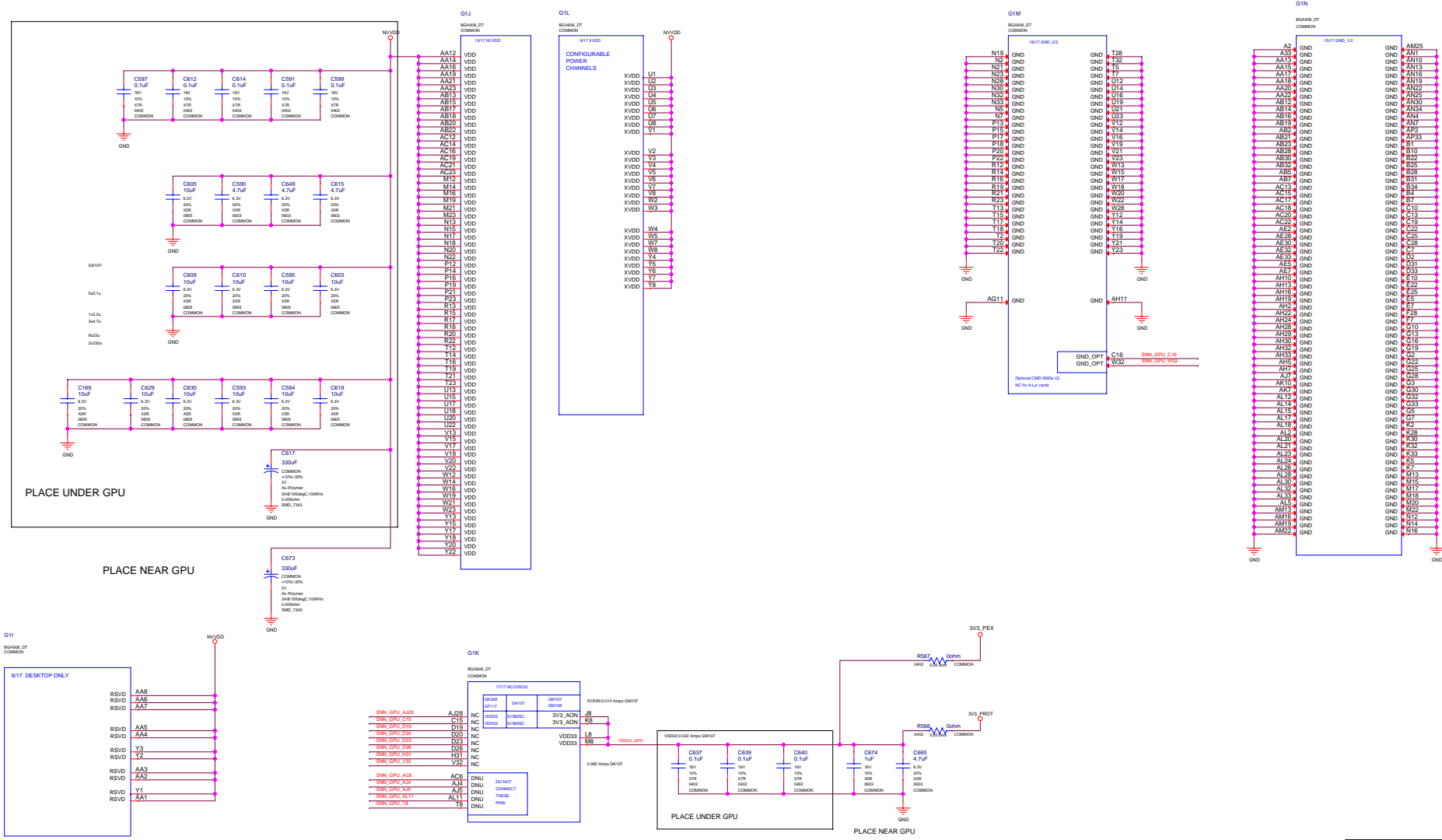
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IFPE Long HDMI

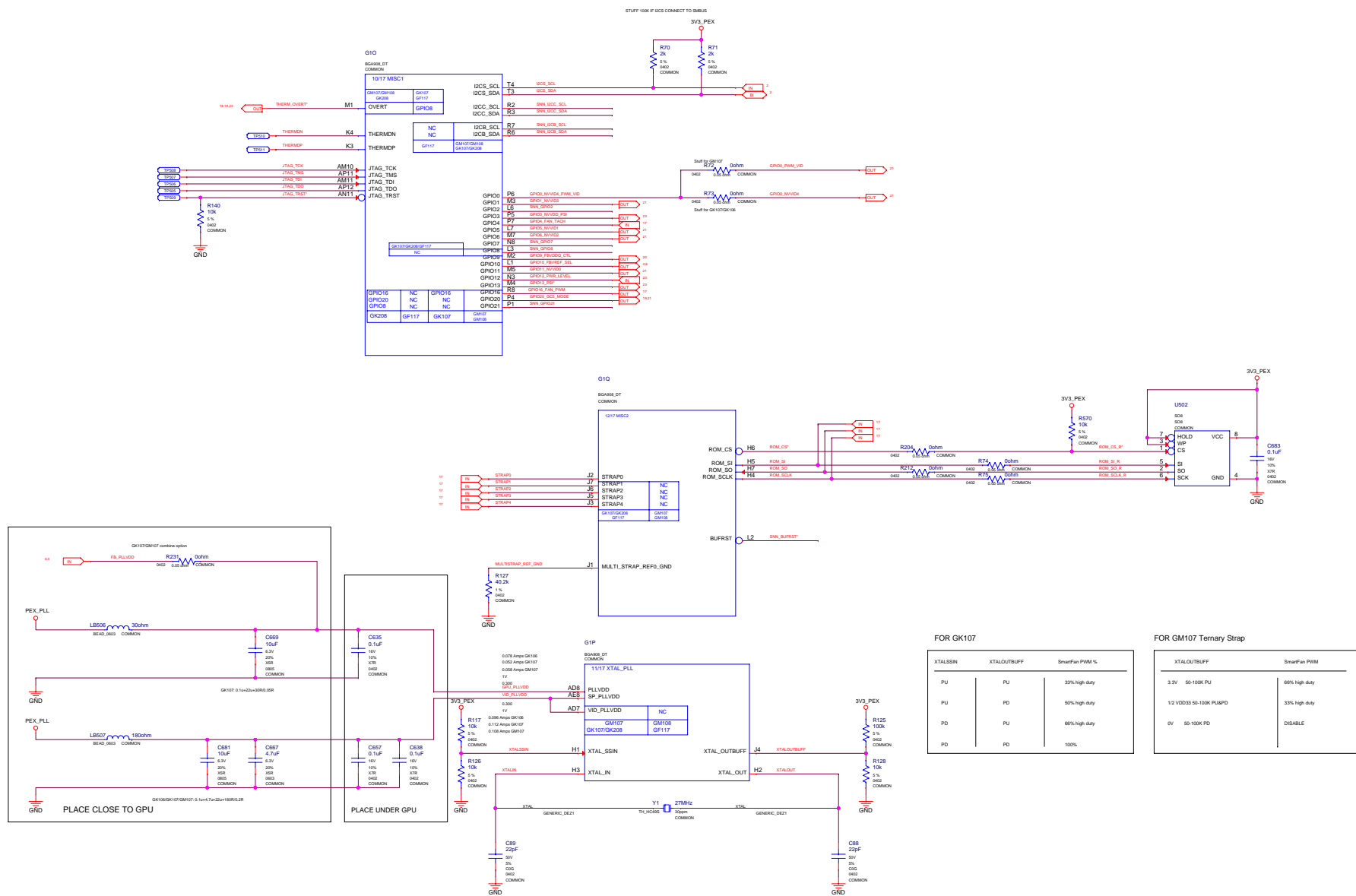


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GPU Core Decoupling, GPU_VDD_3V3



Misc Interfaces



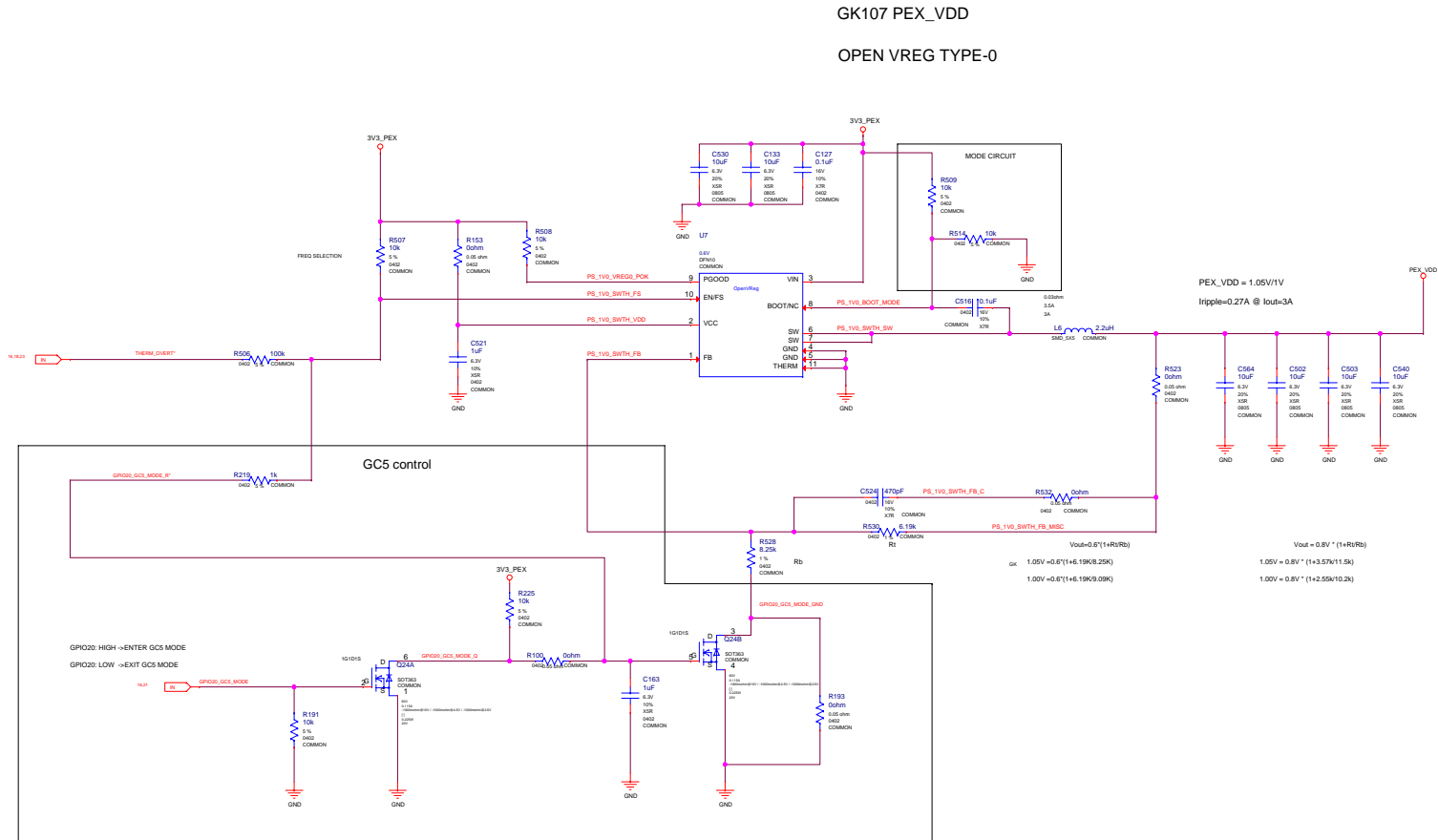
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Power Supply II: PEX_VDD(OPEN VREG)

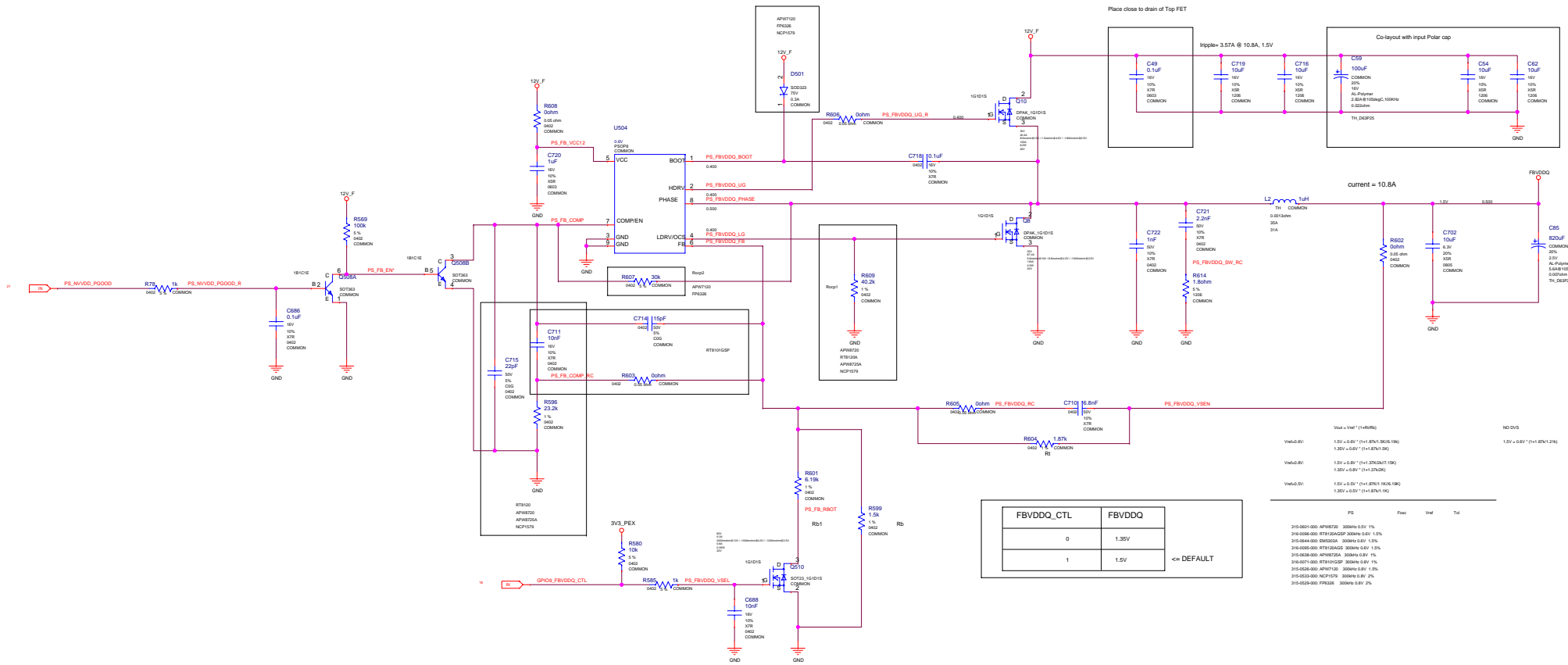


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Power Supply III: FBVDDQ



FBVDDQ_CTL	FBVDDQ
0	1.35V
1	1.5V

<= DEFAULT

	Viol = $V \cdot \ln(1 + \ln(BR))$		
Viol:BY	$1.50 \times 10^{-07} \cdot (1 + 0.8761 \cdot \ln(100))$ $1.305 \times 10^{-07} \cdot (1 + 0.8761 \cdot \ln(90))$		
Viol:BY	$1.50 \times 10^{-07} \cdot (1 + 0.8761 \cdot \ln(100))$ $1.305 \times 10^{-07} \cdot (1 + 0.8761 \cdot \ln(90))$		
Viol:BY	$1.50 \times 10^{-07} \cdot (1 + 0.8761 \cdot \ln(100))$ $1.305 \times 10^{-07} \cdot (1 + 0.8761 \cdot \ln(90))$		
Viol:BY	$1.50 \times 10^{-07} \cdot (1 + 0.8761 \cdot \ln(100))$ $1.305 \times 10^{-07} \cdot (1 + 0.8761 \cdot \ln(90))$		
<hr/>			
PS	Facc	Viol	Tot
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315-0802-02 RT32DAG2	30.004	0.0	1.5%
315-0804-02 EUG0234	30.004	0.0	1.5%
315-0805-02 RT32DAG2	30.004	0.0	1.5%
315-0806-02 APW0232	30.004	0.0	1.5%
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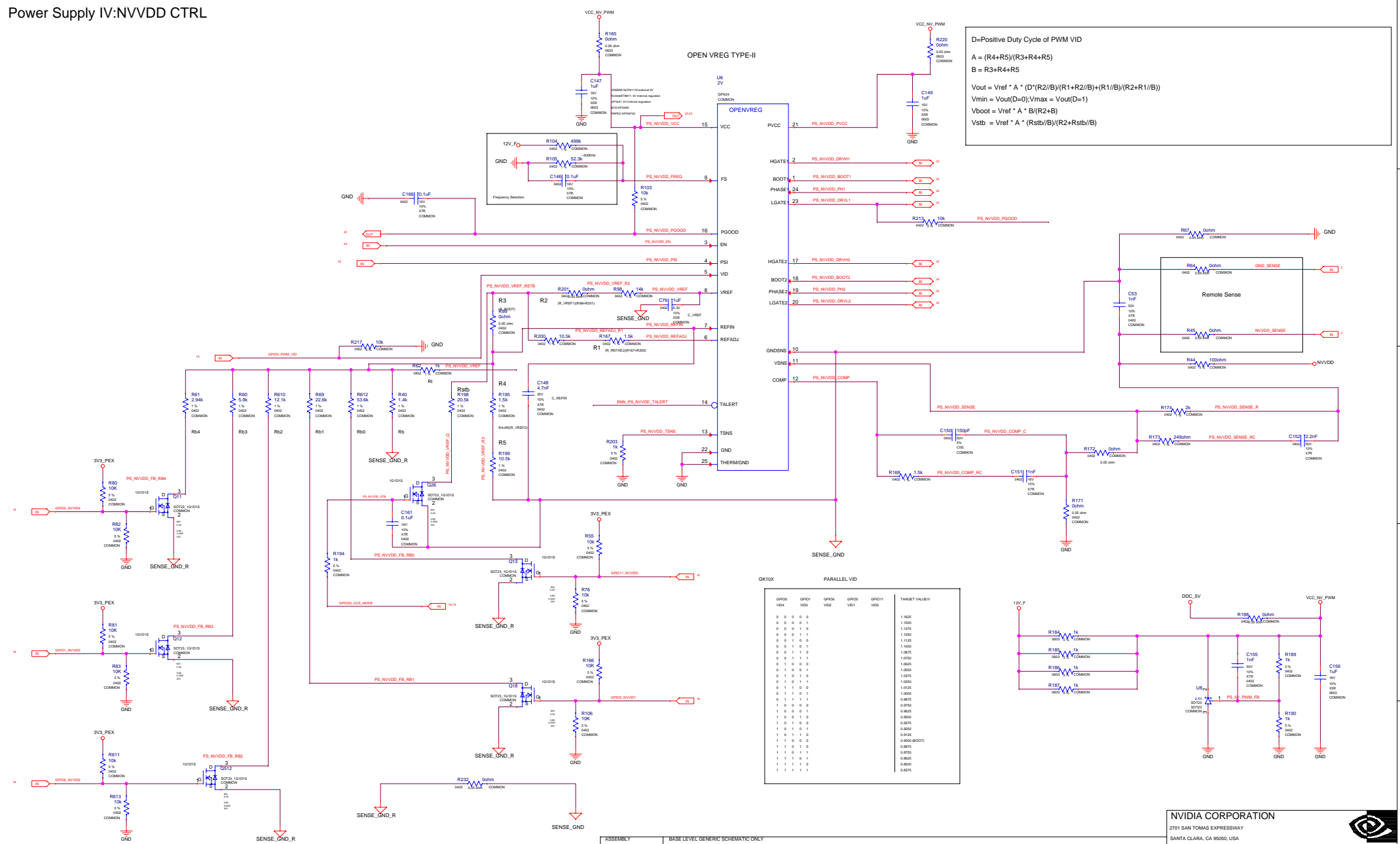
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PCB REV	P2010-B01	PAGE	20 OF 24
BOM REV	A	DATE	28-OCT-2013

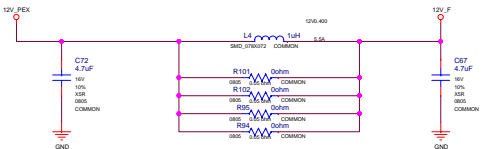
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Power Supply IV:NVVDD CTRL

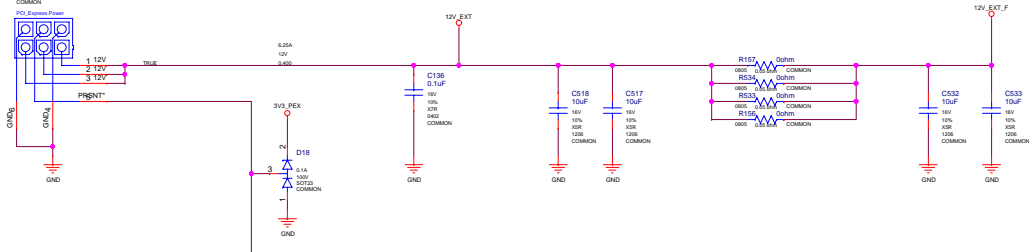
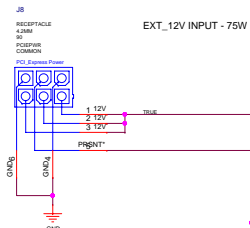
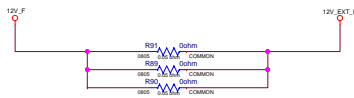


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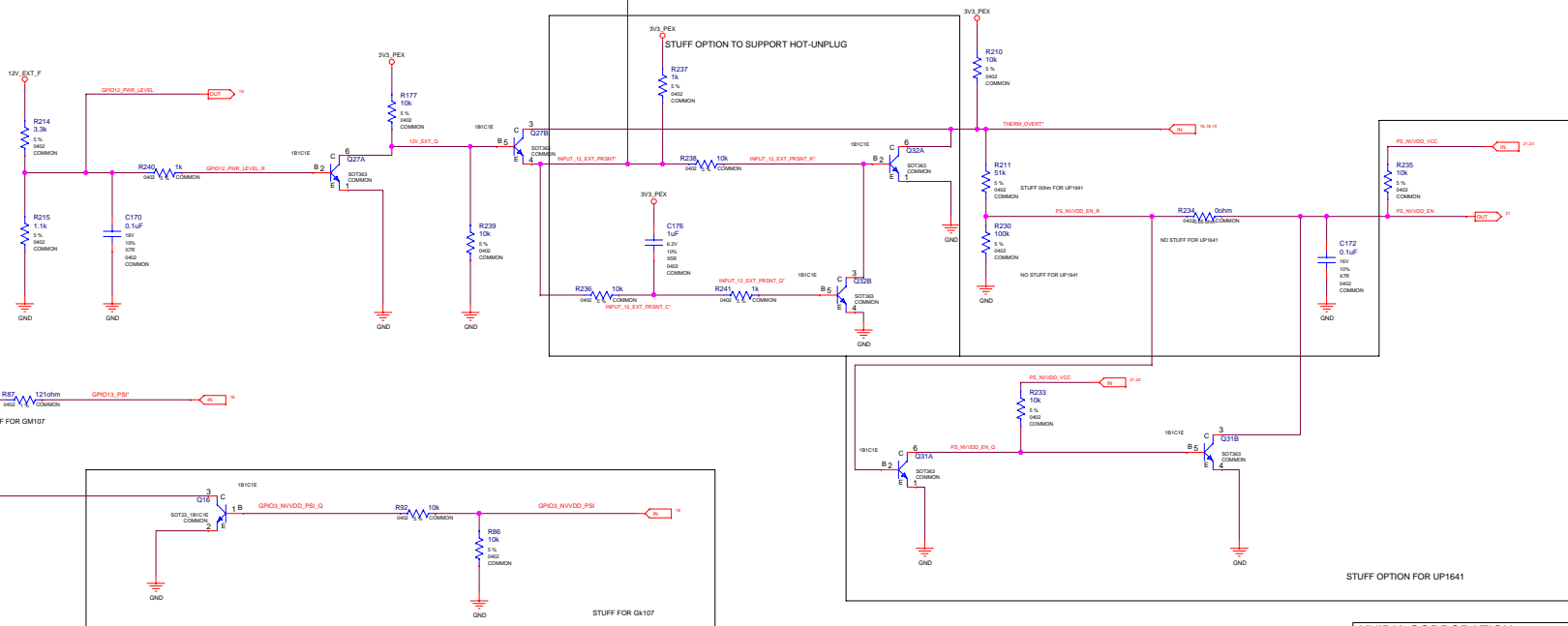
PEX_12V INPUT - 66W



for 75W SKU without 12_EXT



GPIO12_LOW_PERF*	GPU SPEED
0	SLOW
1	NORMAL



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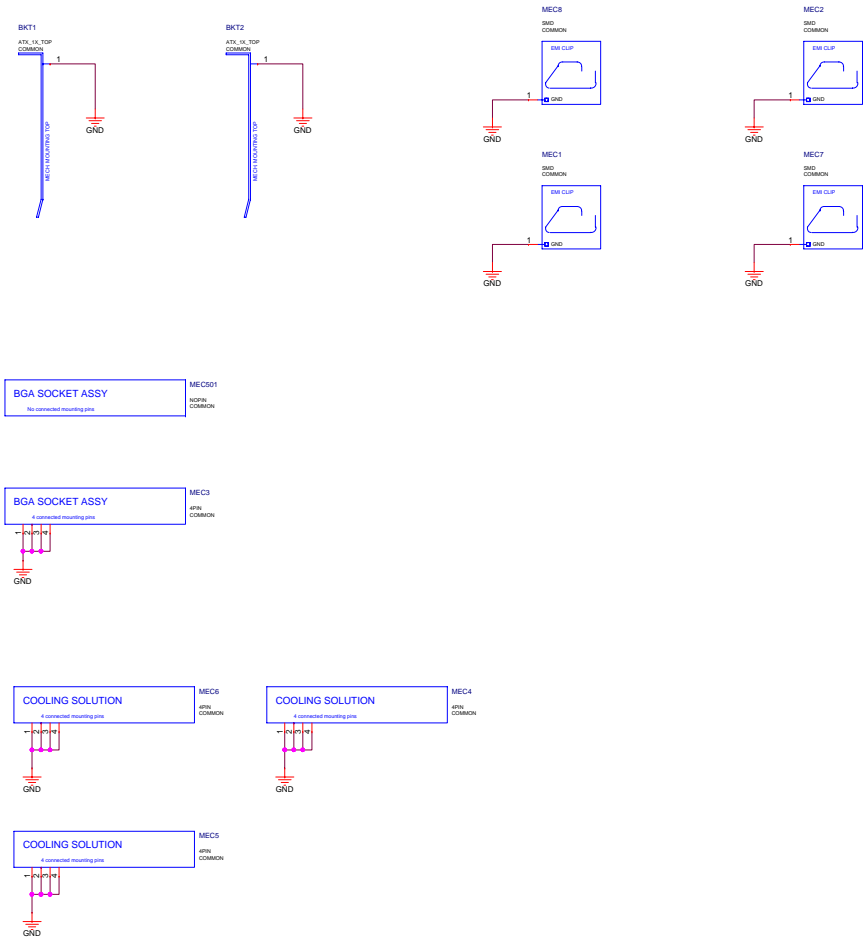
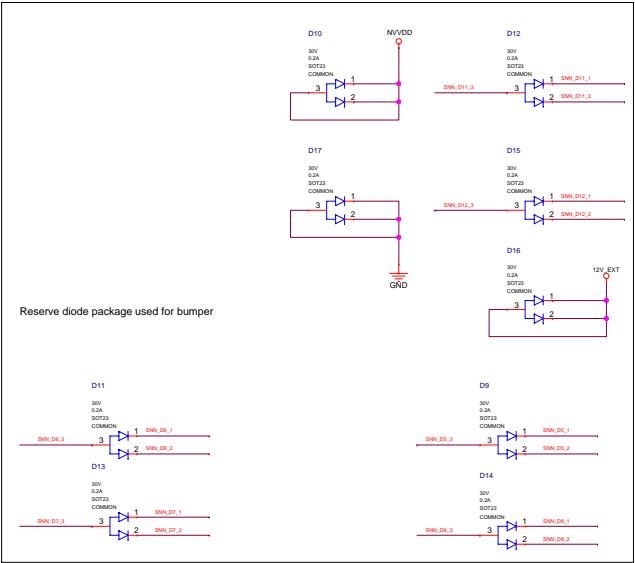
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ASSEMBLY	BASE LEVEL GENERIC SCHEMATIC ONLY
PAGE DETAIL	12V EXT / 12V PEX Power Select Circuit



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